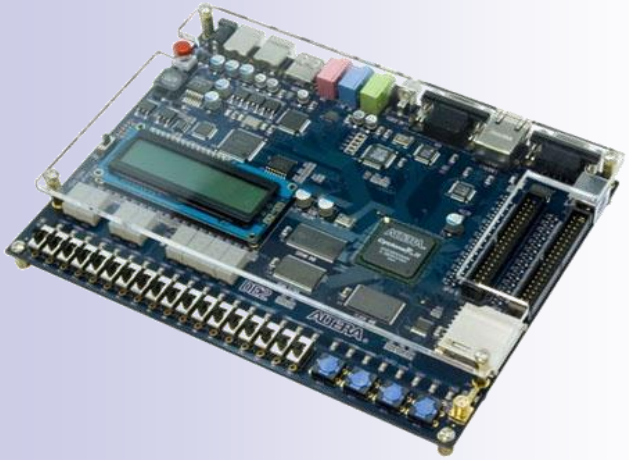
Logic Systems and Processors
cz:Logické systémy a procesory



Version 1.0

CTU-FEE in Prague, CR – subject BE5B35LSP

Lecturer

Richard Šusta main email: richard@susta.cz
(susta@fel.cvut.cz), +420 2 2435 **7359**

Shibboleth.
FELid

https://dcenet.felk.cvut.cz/fpga/
-> login FELId

http://dcenet.felk.cvut.cz/

http://dcenet.felk.cvut.cz/edu/fpga/

<-*Instalations and guides – no login*

Moodle: https://moodle.fel.cvut.cz/course/view.php?id=5164

DCENET = **D**epartment of **C**ontrol **E**ngineering **NET**work

Literature

Lectures + practical exercises

http://dcenet.felk.cvut.cz/edu/fpga/guides.aspx *(public site)*

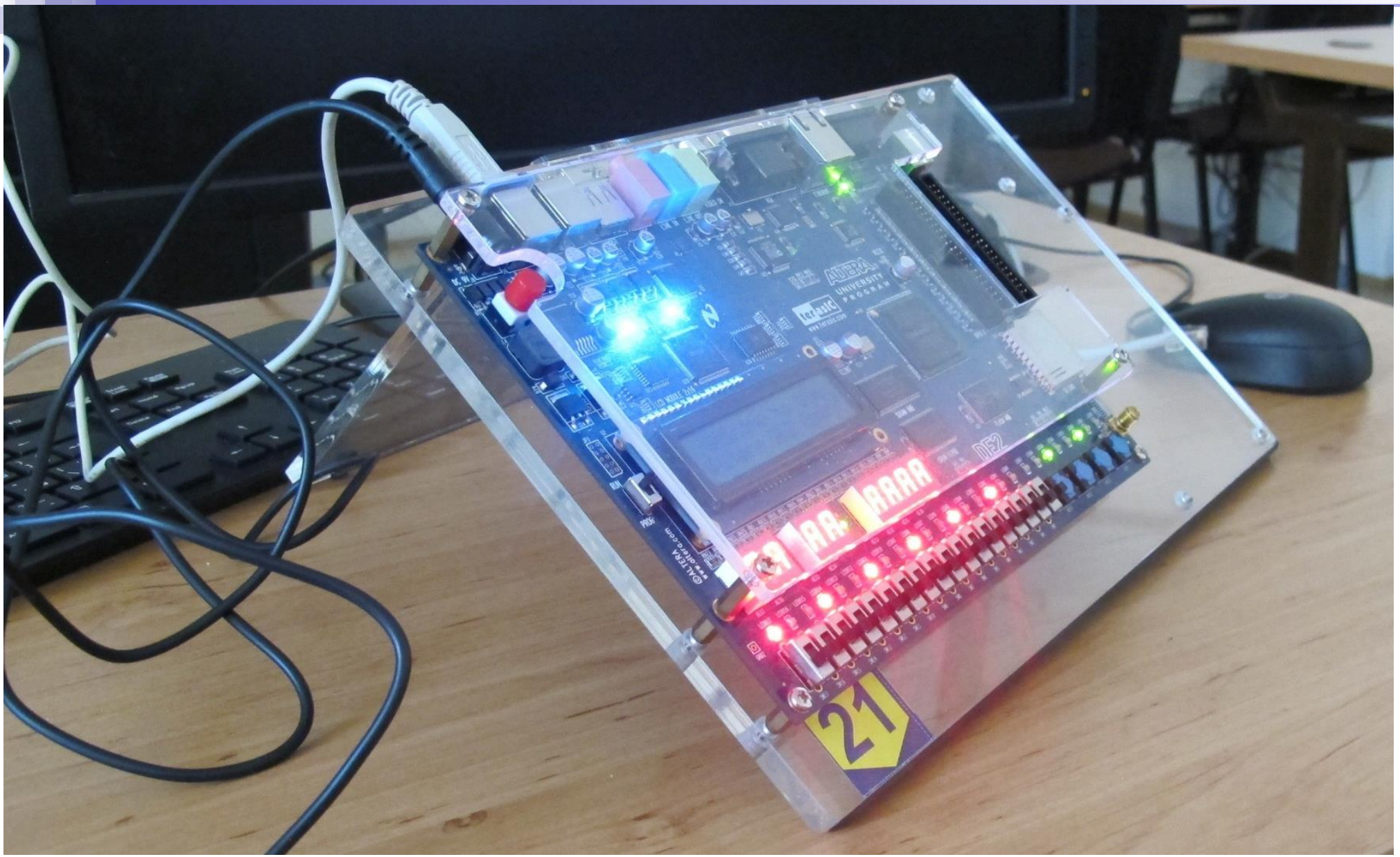
- Šusta, R.: APOLOS prerequisite V1.1, ČVUT-FEL 2019, 51 s.
- Šusta, R.: Circuit Design with VHDL modeling syles "dataflow" and "structural" - english, ČVUT-FEL 2019, 83 s.

https://dcenet.felk.cvut.cz/fpga/ -> Library (**login FelId**)

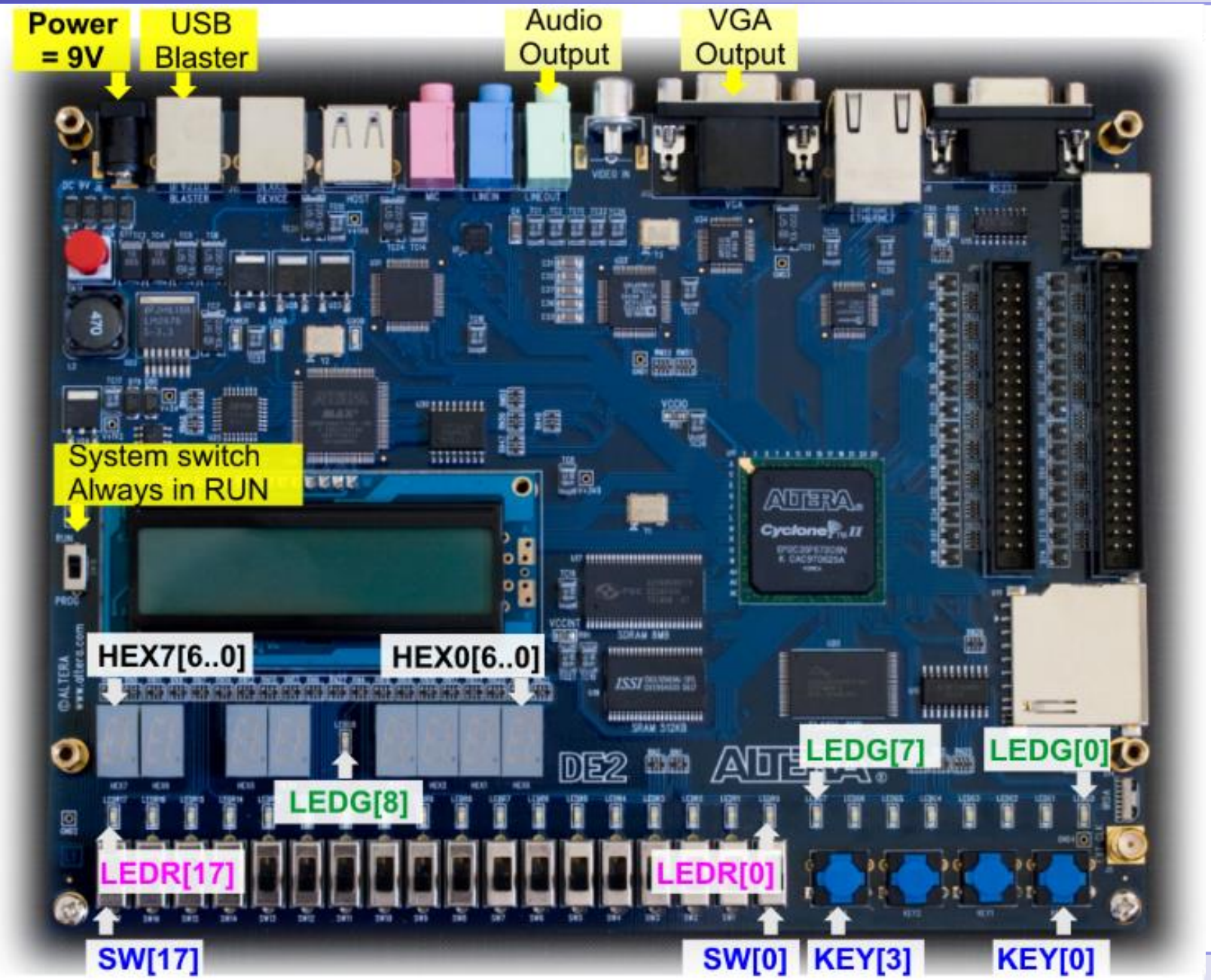
- Enoch O. Hwang: Digital Logic and Microprocessor Design with VHDL, Electronix 2004, 513 s.

FPGA teaching began at FEE in 2009 and has undergone a number of innovations. -> You will find a number of guides and study materials on LSP websites. The list above contains only the most important ones.

DE2 v KN-E-23



DE2 board


Power = 9V
USB Blaster
Audio Output
VGA Output
System switch Always in RUN
HEX7[6..0]
HEX0[6..0]
LEDG[8]
LEDG[7]
LEDG[0]
LEDR[17]
LEDR[0]
SW[17]
SW[0]
KEY[3]
KEY[0]

FPGA

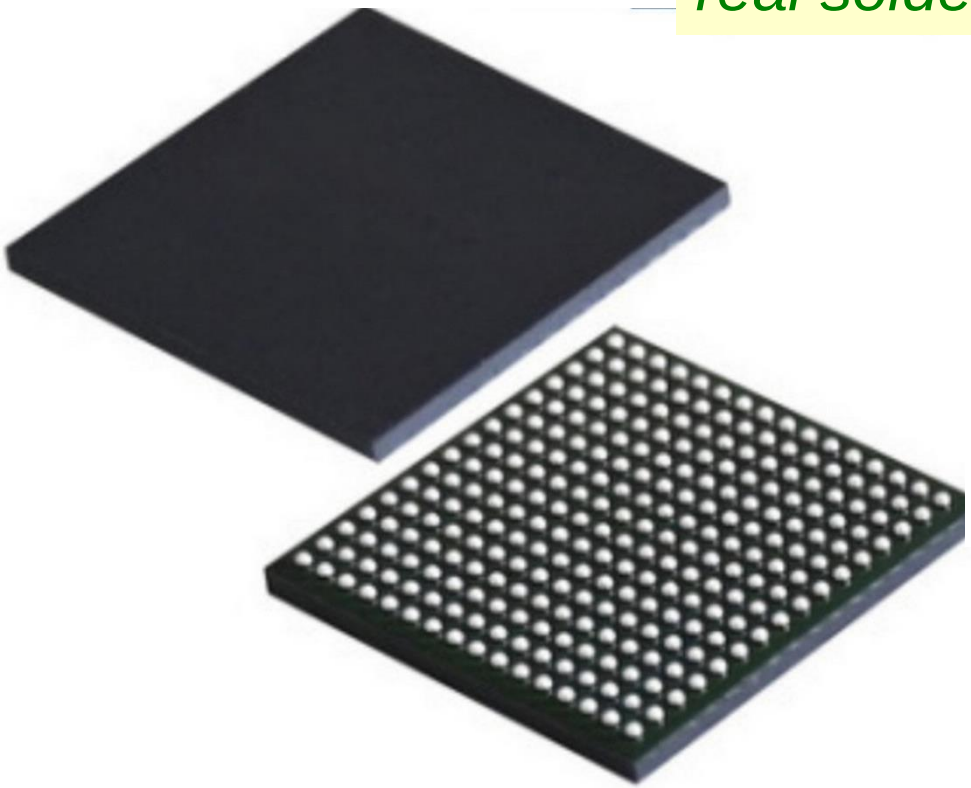
Field

Programmable

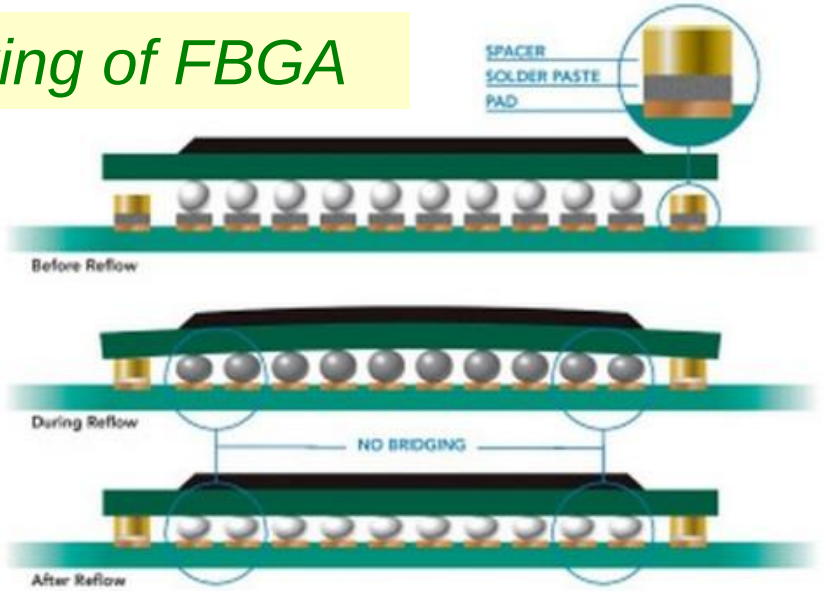
Gate

Array

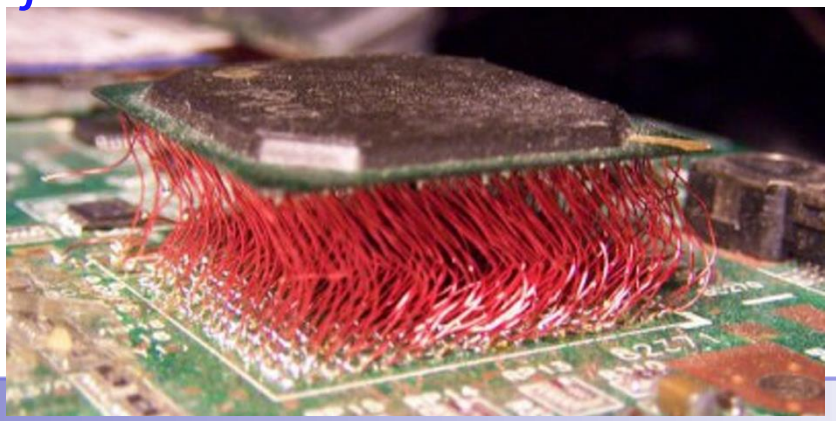
package FBGA - Fine Ball Grid Array



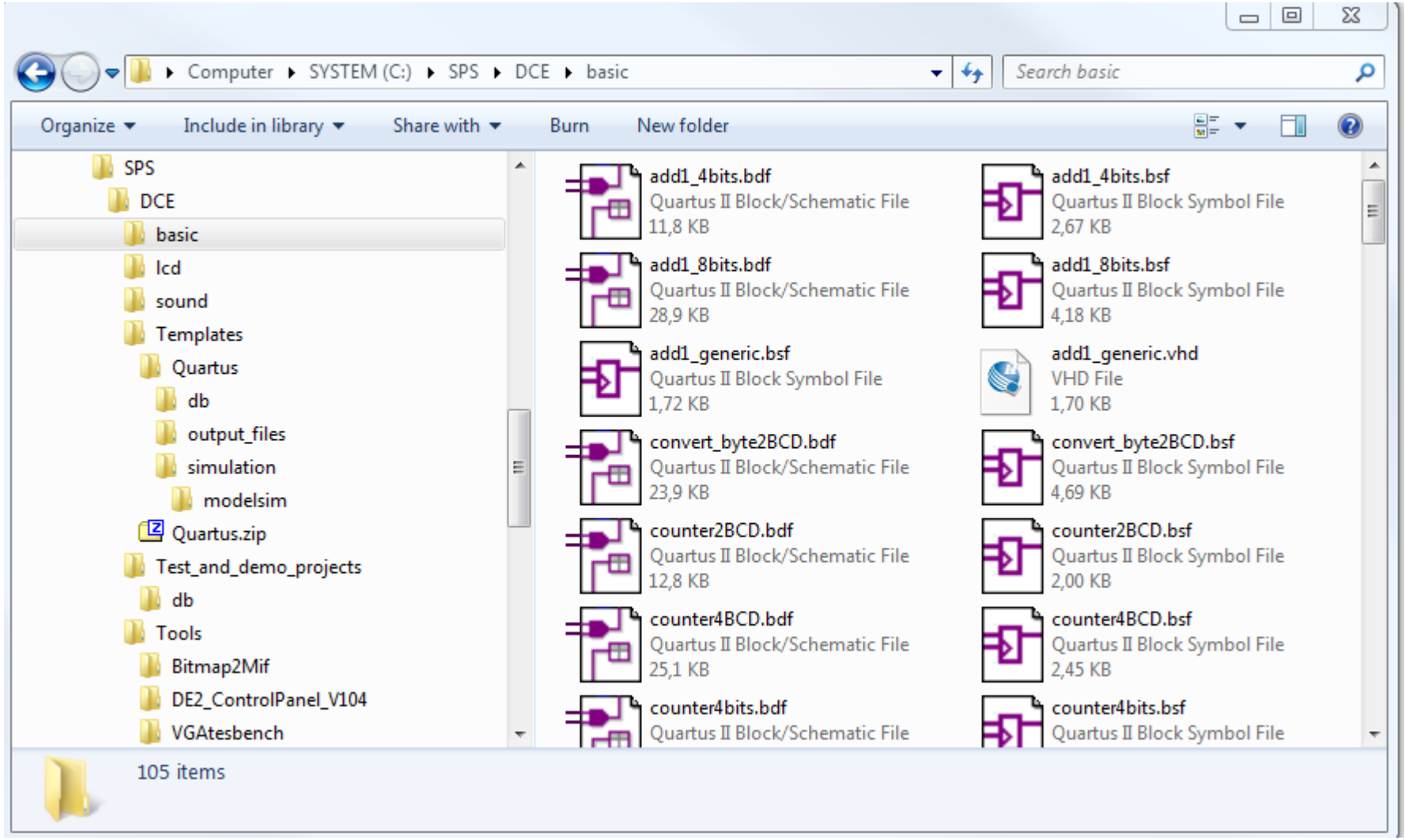
real soldering of FBGA



joke how to solder FBGA



FPGA Libraries DCE



http://dcenet.felk.cvut.cz/edu/fpga/install/DCE_Libraries.zip